

Dr. Devendra Prakash Marathe

Associate Professor

Department of Electronics & Telecommunication Engineering
Godavari College of Engineering, Jalgaon (Maharashtra)

CONTACT

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PERSONAL DETAILS

- Date of Birth : 30 January 1990
- Nationality : Indian
- Marital Status : Married
- Languages : English | Hindi | Marathi

CORE SKILLS

- Embedded Systems & IoT
- VLSI Design
- Python & Machine Learning
- MATLAB
- C / C++ / Data Structures
- Electronics Circuit Design
- Microcontroller (8051, PIC, ARM)
- Research & Technical Writing
- MS Office / LaTeX
- Teaching & Mentoring
- Project Guidance

CERTIFICATIONS & WORKSHOPS

- NPTEL Online Certifications (Various)
- AI & Machine Learning Workshops
- IoT and Robotics Workshop
- Research Methodology (FDP)
- Outcome Based Education (OBE)
- NBA Accreditation Training
- Innovation & Entrepreneurship (IIC)

ACHIEVEMENTS

- First Rank in University Exam – M.E.
- University Representative
- Active Member – Research & IPR
- Organized Technical Events
- Guided B.E./M.E. Projects

AREAS OF INTEREST

- Artificial Intelligence in Healthcare
- Embedded & IoT Systems
- Signal & Image Processing
- VLSI and System Design
- Medical Electronics

PROFILE

Dedicated and result-oriented academic professional with over 15 years of teaching experience in Electronics and Telecommunication Engineering. Research oriented with expertise in Artificial Intelligence applications in healthcare. Committed to academic excellence, innovative teaching methodologies, research, and overall student development.

TEACHING EXPERIENCE **+15 YEARS** (2011 – 2026)

PROFESSIONAL EXPERIENCE

Associate Professor — **2016 – Present**
Department of Electronics & Telecommunication Engineering
Godavari College of Engineering, Jalgaon (Maharashtra)

- Teaching undergraduate and postgraduate courses.
- Guiding B.E./M.E. projects and research activities.
- Contributing to academic planning, curriculum development and NBA/NAAC activities.

Head of Department (HOD) — **2011 – 2015**
Department of Electronics & Telecommunication Engineering
Dr. Varsha Patil Institute of Technology, Jalgaon (Maharashtra)

- Department administration and academic coordination.
- Faculty and student mentoring.
- Organized technical events, workshops and industry interactions.

EDUCATION

Degree	Specialization	University / Institute	Year	Result
Ph.D.	Electronics & Telecommunication	VIT University, Gwalior, M.P.	2026 (03 June 2026)	**Awarded**
Research Topic: *"The Role of AI in Advancing Healthcare Through Improved Diagnosis, Treatment, and Personalized Medicine"*				
MBA	HR & Marketing	K.B.C.N.M.U., Jalgaon	2019 – 2021	75% (Distinction)
M.E.	VLSI and Embedded System Design	North Maharashtra University, Jalgaon	2015	75.25% (Distinction)
B.E.	Electronics & Telecommunication	North Maharashtra University, Jalgaon	2011	72% (Distinction)

RESEARCH PUBLICATIONS

- Published research papers in reputed International Journals (Scopus/UGC).
- Presented and published papers in International and National Conferences.
- Research area: AI in Healthcare, Machine Learning, Signal Processing, Image Analysis.

CONFERENCES & PRESENTATIONS

- Presented research work in various International and National Conferences.
- Participated in FDPs, Workshops and Seminars on AI, IoT, VLSI, and Research Methodology.
- Delivered expert talks and technical sessions in academic and industry platforms.

PROFESSIONAL MEMBERSHIPS

- Life Member – ISTE (Indian Society for Technical Education)
- Member – IEEE (optional if applicable)
- Member – Research and Development Cell
- Active Member – Institution's Innovation Council (IIC)