DR. VIJAY DATTATRAY **CHAUDHARI**

ASSOCATE PROFESSOR (E&TC ENGG.DEPT)

CAREER OBJECTIVE

I am seeking a position with an organization that is rapidly expanding & offers good advancement potential where I can contribute as per my knowledge & skills. I would like a position with a strong emphasis and scope for project, research as well as practical work.

EXPERIENCE (TOTAL 16+ YRS)

- 1. Working as a Associate Professor E&TC Engg dept in GF's Godavari CoE, Jalgaon (01-01-2023 to Till Date)
- 2. Working as a Assistant Professor E&TC Engg dept in GF's Godavari CoE, Jalgaon (May 2010 to 31-12-2022) (NAAC Accre. 'B+') (Approved - KBC North Maharashtra University (KBC NMU), Jalgaon & now Approved Dr. BATU Lonere - Raigad).
- 3. Lecturer (Approved Pune University), Department of Electronics Engg (January 2007 to April 2010), Amrutvahini College of Engg., Sangamner. (NBA 'A' grade & NAAC 'A+')
- **4. Teaching Assistant**, in E&TC Engg dept, Dr. BATU Lonere. (2005).
- **5. Lecturer (chb)**, at Government College of Engg. Jalgaon.
- 6. Lecturer, Industrial Elex dept., Smt. S. S. Patil Polytechnic, Chopda - Dist. Jalgaon. (2002-03)
- 7. Industry Exp.: Videocon International Ltd. Chitegaon, Sambhaji Nagar, M.S. (2001).

ACCOMPLISHMENTS

01 - Springer International Conference As a Session Chair: (DBATU - 2018)

01 - National Conference (ISTE Spons-2017, 2018)

As a Reviewer: 16 - Research papers in 03 Springer International Conferences

21 - Research papers in National & International Conferences and National level Technical Paper presentations.

CONTACT



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- Godavari Foundation's Godavari College of Engineering, P-51, M Sector, Bhusawal Road, Jalgaon-425003, (MS) India.

EDUCATION

Diploma (Industrial Electronics, Sant Muktabai Institute of Technology (S.M.I.T.) Polytechnic, Jalaaon, M.S.), 1997.

DOEACC 'O' Level Professional computer course, DOEACC Society, New Delhi, Govt. of India, Inst. of Information Technology & Research (IITR), Jalgaon (M.S.), 2000.

B.E. (E&TC Engg. Government Engg College, Jalgaon), 2001. M.Tech. (E&TC Engg., Dr. BATU, Lonere-Raigad, Maharashtra), 2006.

Ph.D. (Nov-2024) (Electronics Enga.) – Kavayitri Bahinabai Chaudhari North Maharashtra University, KBC-NMU, Jalgaon, M.S.). Specialization - Embedded Systems & Computing.

SOFTWARE AWARENESS

Triton IDE, C, embedded C, Python, OMNET++, Multisim, EMCad, EMTest, Murata Simulator, EMI-Lator Simulator, MatLab 6.5, Xilinx ISE 10.1, Modelsim 5.4a, Microwind 3.0, Foxpro 2.6, DOS, Windows 98; 2000, 7; XP, MS-Office 2003,07.

SUBJECTS TAUGHT

Embedded Systems, Industrial Internet of Things, VLSI Design, Computer Networks, Microprocessor & Microcontroller As a Resource Person: 02 – Two weeks AICTE Sponsored Faculty Development Program, Amritvahini CoE, Sangamner, M.S.

02 – At Nahata Sci College Bhusawal, (Exert Talk & Lab session)

01 - Home Institute (GF's GCOEJ) (03 days STTP)

01 - Expert talk in National FDP at VBKCOE, Malkapur

01 - Keynote speaker, International Conf . at VBK COE, Malkapur

 $\rm 01-Expert\ talk,\ GF's\ GCOEJ,\ under\ MHRD$ - IIC on

Innovation, Start-up & Entrepreneurship.

Professional Membership:

IEEE, Professional member (98490408). Life Member– ISTE (LM-58949), SIFRE, IJIES (CrossRef DOI) journal.

SUCCESSFULLY ORGANIZED:

International Conferences ... 03 (In association with DBATU & IJIES journal)

National Conferences ... 02 (01 ISTE Sponsored & IJIES journal) National Faculty Development Program.... 04 (Sponsored by DBATU (01), AICTE (01) & IEEE Bombay Section (01)

RESEARCH

Research Paper Published:

International Journals	. :	34
IEEE/ IEEE-ACM Conference	:	03
Springer SCOPUS indexed	:	05
UGC-CARE Journal (Group I/II)	.:	02

PATENTS/BOOKS/PAPER PUBLISHED, PAPER PRESENTED

Patent published:

05; 04 - Govt. of India, 01- Australia Govt. (granted).

Books Authored:

Participated & Presented papers in:

National Conferences: 16 (ISTE Sponsered-07)

International Conferences...: 09 (Springer-04, AICTE-01, ISTE-04)

International Workshop: 02 (01- IEEE Gujrat section).

WORKSHOPS/FDP COUNT

CTTD / EDD /

Participated in:

SIIF/FDF/	
Symposia / Workshops	: 38
AICTE, AICTE-ATÂL, ISTE, Univers	ity
Sponsored STTP/FDP	: 16

AREA OF INTEREST

Embedded Systems, VLSI Design, AI, ML, EMI / EMC

Student's Project Guided:

M.Tech. Students under Guidance: 01
B.Tech. Students under Guidance: 01
M.E./M.Tech students: @29
B.E./B.Tech. students: @47

Student's Seminar Guided:

M.E./M.Tech. students : @23 B.E./B.Tech. students : @30 T.E. (E&TC) students : @20